

**PROCESSOR TO PROCESSOR COMMUNICATION  
IN A DATA DRIVEN ARCHITECTURE**

**Abstract**

5           In an embodiment, an apparatus includes a first processor that includes a  
first processor element. The apparatus also includes a second processor that  
includes a second processor element. The first processor is configured to transmit  
data to the second processor through a third processor, wherein no processor  
element within the third processor is configured to perform a process operation on  
10 the data as part of the transmission of the data from the first processor to the second  
processor.

"Express Mail" mailing label number: EV 332571736 US  
Date of Deposit: June 19, 2003  
This paper or fee is being deposited on the date indicated above with  
the United States Postal Service pursuant to 37 CFR 1.10, and is  
addressed to the Commissioner for Patents, Mail Stop Patent  
Application, P.O. Box 1450, Alexandria, VA 22313-1450.